

**Notice of References Cited**

Application/Control No.

09/702,112

Applicant(s)/Patent Under  
Reexamination  
JENSEN ET AL.

Examiner

Richard Ellis

Art Unit

2183

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,829,012 A	10-1998	Marlan et al.	711/102
	B	US-4,802,119	01-1989	Heene et al.	714/7
	C	US-4,603,399	07-1986	Cheek et al.	711/202
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM Technical Disclosure Bulletin, Patchable Read-Only Storage and Other Patchable Functions, November 1, 1984, V 27, I 6, pp. 3496-3499.
	V	IBM Technical Disclosure Bulletin, Patch RAM Load Technique, November 1, 1984, V 27, I 6, pp. 3597-3598.
	W	IBM Technical Disclosure Bulletin, Microcode Memory Changes, June 1, 1978, V 21, I 1, pp. 341-342.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.